

**In the Claims:**

1-9. (canceled)

10. (previously presented) A method of processing signals for a communication system, comprising the steps of:

receiving an input signal from at least one of a plurality of antennas;

measuring the input signal;

producing an output signal corresponding to the measured input signal;

comparing the output signal to a first reference signal;

producing a first control signal indicating one of transmit diversity and no transmit diversity of the input signal in response to the step of comparing when the output signal has a value greater than a value of the first reference signal;

comparing the output signal to a second reference signal; and

producing a second control signal indicating the other of transmit diversity and no transmit diversity of the input signal in response to the step of comparing when the output signal has a value less than a value of the second reference signal.

11. (original) A method as in claim 10, further comprising the step of producing a third control signal in response to the step of comparing when the output signal has a value between the value of the first reference signal and the value of the second reference signal.

12. (previously presented) A method as in claim 10, further comprising the steps of:

producing a plurality of channel estimates in response to one of the first control signal and the second control signal; and

producing at least one channel estimate less than the plurality of channel estimates in response to the other of the first control signal and the second control signal.

13-24. (canceled)

25. (currently amended) A circuit, comprising:

an estimate circuit coupled to receive an input signal from at least one of a plurality of transmit antennas and coupled to receive a control signal, the control signal corresponding to a number of the at least one of a plurality of transmit antennas, the estimate circuit selectively producing a first estimate signal and a second estimate signal in response to the control signal;

a correction circuit coupled to receive the input signal, the first estimate signal and the second estimate signal, the correction circuit producing a corrected input signal;

a combiner circuit coupled to receive the corrected input signal, the combiner circuit producing a combined input signal; ~~and~~

a decoder circuit coupled to receive the combined input signal, the decoder circuit arranged to decode the combined input signal, thereby producing the control signal;

a measurement circuit coupled to receive the input signal, the measurement circuit producing an output signal corresponding to the input signal; and

a comparator circuit coupled to receive the output signal, a first reference signal and a second reference signal, the comparator circuit arranged to produce a second control signal in response to a comparison of the output signal, the first reference signal and the second reference signal.

26. (canceled)

27. (previously presented) A circuit as in claim 25, wherein the input signal comprises at least one pilot symbol of a wideband code division multiple access signal.

28. (previously presented) A circuit as in claim 25, wherein the control signal comprises a transmit diversity signal.

29-31. (canceled)